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W	AA1	3,34	6,851	10/1967	Thornton et al.			
n	AB1	 	1,528	11/1970	Randell			
~	AC1	3,77	1,138	11/1973	Celtruda et al.			
n	AD1	3,78	39,365	01/1974	Jen et al.			
\sim	AE1	3,91	6,388	10/1975	Shimp et al.			
9	AF1	 	34,349	07/1977	Monaco et al.			
v~	AG1	 	34,235	04/1978	Hirtle			
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\sim	AK1	0 41 A2/A	19 105 A3	03/1991	EPO	<u> </u>	+ ->	Yes No
0	AL1	0 40	02 856 B1	12/1990	ЕРО			Yes No
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1	AO	<u>1</u>	Agerwala Research	et al., "High Division, Marc	Performance Reduced Instruc h 31, 1987, pp. 1-61.	ction Set	Processor	s," IBM
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n	AB2	4,2	00,927	04/1980	Hughes et al.			<u> </u>		
cv	AC2	4,2	28,495	10/1980	Bernhard et al.			<u> </u>		
~	AD2	4,2	36,206	11/1980	Strecker et al.			ļ	 	
~	AE2	4,2	96,470	10/1981	Fairchild et al.			<u> </u>	<u> </u>	
	AF2	4,3	15,314	02/1982	Russo				 	
<u>~</u>	AG2	4,3	17,170	02/1982	Wada et al.			<u> </u>	 	
4	AH2	4,3	96,982	08/1983	Wada et al.			<u> </u>	<u> </u>	
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N	АЈ2	0 3 A2/	72 751 A3	06/1990	EPO				Yes No	
\sim	AK2	0 3	68 332 B1	05/1990	EPO				Yes No	
	AL2	0 3	54 585 B1	02/1990	EPO		J		Yes No	
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W	AR	2	Crawford, J., "The Execution Pipeline of the Intel i486 CPU," IEEE, 1990, pp. 254-258.							
N	As	<u>2</u>	Faix, M. and Schuenemann, C., "Combined Macro/Micro Program Machine," IBM Technical Disclosure Bulletin, Vol. 14, No. 1, June 1971, p. 298.							
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4	AA3	4,41	0,939	10/1983	Kawakami	<u> </u>		
n	AB3	4,43	4,461	02/1984	Puhl			
~	AC3	4,45	4,578	06/1984	Matsumoto et al.			L
5	AD3	4,45	6,955	06/1984	Yanagita et al.			
W	AE3	4,45	9,657	07/1984	Murao			<u></u>
2/	AF3	4,47	76,525	10/1984	Ishii			ļ.———-
ω	AG3	4,51	14,803	04/1985	Agnew et al.			
5	АН3	4,56	59,016	02/1986	Hao et al.			
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4/	АЈЗ	0 13	36 179 A2	04/1985	EPO			Yes No
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6	AL3	2 01	11 682 A	07/1979	GB			Yes No
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6	AN	<u>3</u>	Garth, S.O	C.J., "Combini	ng RISC and CISC in PC Syst	cems," I	EEE, pp. 10	/1-10/5.
4	АО	<u>3</u>	Goodman, o Basic Bloo	J.R. and Hsu, cks," <i>ACM</i> , 198	W., "Code Scheduling and Re 18, pp. 442-452.	egister .	Allocation	in Large
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cr	AA4	4,58	7,612	05/1986	Fisk et al.	_		<u> </u>			
9	AB4	4,62	6,989	12/1986	Torii	_		<u> </u>			
7	AC4	4,65	4,781	03/1987	Schwartz et al.			<u> </u>			
4	AD4	4,67	5,806	06/1987	Uchida						
2	AE4	4,71	4,994	12/1987	Oklobdzija et al.			ļ			
~	AF4	4,72	2,049	01/1988	Lahti	_					
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20	AI4	0 17	70 398 A3	02/1986	EPO				Yes: No		
. 4	AJ4	0 21	72 198 A2	06/1988	ЕРО				Yes No		
2	AK4	0 38	30 854 A3	08/1990	EPO				Yes- No		
9	AL4	2 2:	30 116 A	10/1990	GB				Yes No		
	OTHER (Including Author, Title, Date, Pertinent Pages, etc.)										
a	АМ	4	Heath, S. processor	, "Architectur ," pp. 6/1-6/6	ral considerations for a M8 5.	38000	super	scalar RI	sc		
~	AN	4	Hennessy Publisher	et al., <u>Comput</u> s, Inc., 1990	ter Architecture: A Quantit (in its entirety).	ative	Appr	oach, Mor	gan Kaufmann		
4	AO	<u>4</u>	Horst, R.	W. et al., "Mu ," <i>IEEE</i> , 1990,	ultiple Instruction Issue i , pp. 216-226.	in the	NonS	top Cyclo	ne		
4	AR	4	Hwu et al Machines, 1514.	., "Checkpoint " IEEE Trans.	t Repair for High-Peformand On Computers, Vol. C-36, h	ce Out No. 12	-of-0 , Dec	order Exec ember 198	oution 7, pp. 1496-		
6	AS	<u>4</u>	Compiler	., "Exploiting Code Generator ure, June 1986	g Parallel Microprocessor N r," Proceedings of the 15th 8, pp. 45-53.	h Annu	al Sy	mposium c	with a on Computer		
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1/	AA5	4,73	9,472	04/1988	Hayashi		<u> </u>	
N	AB5	4,75	2,873	06/1988	Shonai et al.			
a	AC5	4,75	8,948	07/1988	May et al.	<u> </u>	<u> </u>	
2	AD5	4,76	0,519	07/1988	Papworth et al.	<u> </u>		
~	AE5	4,76	6,566	08/1988	Chuang		<u> </u>	
\sim	AF5	4,77	71,376	09/1988	Kamiya			
~	AG5	4,77	77,592	10/1988	Yano			
	AH5	4,80	7,115	02/1989	Torng			<u>. </u>
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EXAMINER INITIAL		DOCU	JMENT	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
(/	AI5	+	51 257 A1	12/1991	ЕРО			Yes, No
~	AJ5	0 4	73 420	03/1992	ЕРО			Yes No
\sim	AK5	0 14	47 775	07/1985	EPO		-	Yes No
\sim	AL5	3 60	03 240	08/1987	DE			Yes No
	<u> </u>	-	OTHER (Inc.	luding Author,	Title, Date, Pertinent Pag	ges, etc.)		
α	АМ	<u>5</u>	Hwu et al Minimal F pp. 297-3	unctionality,'	igh Performance Restricted Proceedings from ISCA-13,	Data Flow Tokyo, Ja	Architectu apan, June	re Having 2-5, 1986,
. W	AN	<u>5</u>	i486™ Mic Corporati	roprocessor Ha	ardware Reference Manual, O ca, California, 1990.	rder No. 2	240552, In	tel
4	АО	<u>5</u>	i486™ Mic Corporati	roprocessor P on, Santa Cla	rogrammer's Reference Manua ra, California, 1990.	l, Order	No. 240486	. Intel .
W	AR	<u>5</u>	Time Cont	y, J. et al., rol Applications, CA, pp. 1	"New Microcomputer Feature ons," <i>Electro Conference Re</i> -9.	s Advance cord, Vol	d Hardware . 14, Apri	For Real- l 11, 1989,
4	AS	5	Johnson,	M., Superscal	ar Microprocessor Design, P			1991.
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4	AA6	4,81	4,976	03/1989	Hansen et al.			
~	AB6	4,85	8,105	08/1989	Kuriyama et al.			
n	AC6	4,89	7,810	01/1990	Nix	ļ		
~	AD6	4,90	1,228	02/1990	Kodama	<u> </u>		
w	AE6	4,90	3,196	02/1990	Pomerene et al.	ļ		
~	AF6	4,92	4,376	05/1990	Ooi			
~	AG6	4,92	6,323	05/1990	Baror et al.			
N	AH6	4,94	2,520	07/1990	Langendorf			
				FOREIG	N PATENT DOCUMENTS		_	_
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61	AI6	0 25	SER 59 095	03/1988	EPO		CLASS	Yes No
	AJ6	0 27	2 198	06/1988	EPO		-	Yes No
	AK6	0 30	2 999	02/1989	EPO	_	>	Yes. No
0	AL6	0 37	77 990	07/1990	EPO	_		Yes
			AMITTED (Total	ludine Author	Title, Date, Pertinent Pag	es. etc.)	<u> </u>	
	АМ	<u>6</u>	Johnson, 1989, 134		per-Scalar Processor Design	ı, (Disse	rtation),	Copyright
W	AN	61	Cuparnina	lined Machines ural Support i	ole Instruction-Level Parall ," Proceedings of the 3rd i for Programming Languages an	Internati	onal Confe	rence on s, April -
W	AO	<u>6</u>	Jouppi, N	.H., "Integrat 9, pp. 229-232	ion and Packaging Plateaus ?.	of Proce	ssor Perfo	rmance,"
	AR	<u>6</u>	Paralleli	sm and Its Eff	uniform Distribution of Inst Eect on Performance," <i>IEEE</i> : 989, pp. 1645-1658.	ruction- Transacti	Level and ons on Com	Machine puters, Vol.
	AS 6 Keller, R.M., "Look-Ahead Processors," Computing Surveys, Vol. 7, No. 4, December 1975, pp. 177-195.							
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~	AA7	4,94	12,525	07/1990	Shintani et al.		<u> </u>	<u> </u>	
2	AB7	4,98	35,825	01/1991	Webb, Jr. et al.				
S.	AC7	4,99	92,931	02/1991	Hirasawa				
1	AD7	4,99	2,934	02/1991	Portanova et al.				
\sim	AE7	4,99	92,938	02/1991	Coke et al.			<u> </u>	
N	AF7	5,00	3,462	03/1991	Blaner et al.	<u></u>	<u> </u>		
N	AG7	5,09	51,940	09/1991	Vassiliadis et al.	<u> </u>			
9/	AH7	5,07	75,849	12/1991	Kuriyama et al.				
				POREIG	N PATENT DOCUMENTS				
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W	AI7	+	36 092	07/1991	EPO			Yes: No	
~)	AJ7	0 40	65 321 A2	01/1992	ЕРО	-	<u> </u>	Yes No	
W	AK7	GB :	2 241 801	09/1991	United Kingdom			Yes No	
\sim	AL7	JP :	2-148238	06/1990	Japan			Yes No	
			OTHER (Incl	luding Author,	Title, Date, Pertinent Pag	es, etc.)			
	ΑМ	2	Lam, M.S.	, "Instruction ci., Vol. 4, 1	Scheduling For Superscalar 990, pp. 173-201.	Architec	tures," A	nnu. Rev.	
W	AN	<u>7</u>	Lee, R.B.	, "Precision A	rchitecture," <i>COMPUTER</i> , Jan	uary 1989	, pp. 78-	91.	
W	AO	7		S., "The i9600 , pp. 232-240.	CA Superscalar Implementation	on of the	80960 Arc	hitecture",	
W	AR	<u>7</u>	Computer	K.J. and Milut with a Reduced bruary 1987.	inovic, V.M., "Emulating a I Instruction Set Computer,"	Complex I	nstructio ro, Vol.	on Set 7, No. 1, pp.	
	AS Z Melear, C., "The Design of the 88000 RISC Family," IEEE MICRO, Vol. 9, No. 2, April 1989, Los Alamitos, CA, pp. 26-38.								
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W	AA8	+	01,341	03/1992	Circella et al.				3		
~	AB8	+	27,091	06/1992	Boufarah et al.						
W	AC8		33,072	07/1992	Buzbee						
\sim	AD8		33,077	07/1992	Karne et al.						
~	AE8		48,528	09/1992	Fite et al.						
W	AF8		48,536	09/1992	Witek et al.						
~	AG8		55,843	10/1992	Stamm et al.						
0	АНВ		68,571	12/1992	Hoover et al.						
	711.0	1/-		<u> </u>	N PATENT DOCUMENTS						
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~	AI8		4-77925	03/1992	Japan			¬	Yes No		
0,0	AJ8		4-219825	08/1992	Japan		_	$\overline{}$	Yes No		
N	AK8	JР	58-217054	12/1983	Japan				Yes- No		
1	AL8	JР	59-154548	09/1984	Japan				Yes No		
	OTHER (Including Author, Title, Date, Pertinent Pages, etc.)										
40	АМ	<u>8</u>	Murakami, Pipelinin 78-85.	K. et al., "S g): A Novel Hi	SIMP (Single Instructions)	on stream ssor Arch	/Multi; itectu:	ole instru re," <i>ACM</i> ,	action 1989, pp.		
	AN	8	Nakano, H IBEE 1991	. et al., "A & Custom Integi	30 Mflops 64-bit Micro rated Circuits Confere	processor nce, pp.	for Pa 15.2.1	arallel Co	omputer",		
	AO	<u>8</u>	Workstati	. et al., "Ard ons," Proceed: , CA, pp. 214	chitecture and Compile ings from IEEE Compcon -218.	r Enhance , Februar	ments : y 25 -	for PA-RIS March 1,	SC 1991, San		
W	AR 8 Ohr, S., "Superlong instructions help supermini refine number crunching," Electronic Design, Vol. 33, No. 26, November 1985.										
· W	AS B "Overlap of Store Multiple Operation With Succeeding Operations Through Second Set of General Purpose Registers," IBM Technical Disclosure Bulletin, Vol. 28, No. 6, November 1989, New York, NY, pp. 2727-2728.										
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. 4	AA9	5,18	5,868	02/1992	Tran			
	AB9	5,19	3,206	03/1993	Mills			
W	AC9	5,22	26,126	07/1993	McFarland et al.			
W	AD9	5,23	0,068	07/1993	Van Dyke et al.	<u> </u>		
N	AE9	5,25	31,306	10/1993	Tran			
W	AF9	5,31	17,740	05/1994	Sites	ļ		
U	AG9	5,32	23,489	06/1994	Bird			
97	АН9	5,35	55,460	10/1994	Eickenmeyer et al.		<u> </u>	
				FOREIG	N PATENT DOCUMENTS			
EXAMINER INITIAL				DATE	COUNTRY	CLASS		TRANSLATION
4	AI9	61-3	33546	02/1986	Japan			,
1/2	AJ9	1-28	36030	11/1989	Japan			
12	АК9	2-13	30634	05/1990	Japan		_	1
1)	AL9					-		
			OTHER (Incl	uding Author,	Title, Date, Pertinent Pag	es, etc.)		•
\bigcirc	АМ	2	Patent Abs	stracts of Jap	oan, Vol. 017, No. 082 (P-14	89), Febi	cuary 18,	1993 .
W	AN	9	Patent Abi	stracts of Jap	oan, Vol. 017, No. 084 (P-14	90), Febi	ruary 19,	1993.
W	AO	<u>9</u>	Patent Abs	stracts of Jap	pan, Vol. 009, No. 006 (P-32	26), Janua	ary 11, 19	85.
W	AR	<u>9</u>	Patent Abs	stracts of Jap Seisakusho K P	pan, Vol. 010, No. 089, Apri K) 11 November 1985.	.1 1986, 8	â Ј Р, А, б	0 225 943
M	AS	<u>9</u>	Microarch:	l., "Critical itecture," Pro 1985, pp. 109-	oceedings of the 18th Annua -116.	l Workshop	o on/Micro	programming,
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1.0	AA10	5,37	71,684	12/1994	Iadonato et al.			·
9/	AB10	5,39	0,355	02/1995	Horst			
7	AC10	5,44	2,757	08/1995	McFarland et al.	ļ. <u></u>		
in.	AD10	5,48	37,156	01/1996	Popescu et al.			
v.	AE10	5,53	9,911	07/1996	Nguyen et al.			
	AF10	5,59	57,763	09/1996	Senter et al.			
ov ,	AG10	5,56	51,776	10/1996	Popescu et al.			
9	AH10	5,57	74,927	11/1996	Scantlin	<u> </u>		
				FOREIG	N PATENT DOCUMENTS			
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	AI10							Yes No
	AJ10							Yes No
	AK10							Yes No
	AL10							Yes No
			OTHER (Inc.)	luding Author.	Title, Date, Pertinent Pag	es, etc.)	<u> </u>	<u> </u>
W,	АМ	10	Patt et a Proceeding 103-108.	l., "HPS, A Ne gs of the 18th	w Microarchitecture: Ration Annual Workshop on Micropr	ale and I ogramming	ntroductio , December	on," 1985, pp.
W .	AN	<u>10</u>	Processor	et al., "The F s," Proceeding , pp. 37-44.	Performance Potential of Mul s of the 15th Annual Sympos	tiple Fun	ctional Un	nit chitecture,
W	OA	10	Pleszkun the 14th	et al., "WISQ: International	A Restartable Architecture Symposium on Computer Archi	Using Qu tecture,	eues," Pro June 1987	pp. 290-
M	AR	10	Popescu,	V. et al., "Th	ne Metaflow Architecture," l	EEE Micro	, June 199	91, pp. 10-13
	AS	10		D. <i>ety al</i> ., "Bo ," <i>IEEE</i> , 1990,				lar A
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N	AA11	5,59	2,636	01/1997	Popescu et al.	<u> </u>		
S	AB11	5,60	6,676	02/1997	Grochowski et al.	<u> </u>		·.
W	AC11	5,61	9,668	04/1997	Zaidi			
v	AD11	5,62	5,837	04/1997	Popescu et al.			
VO.	AE11	5,62	7,983	05/1997	Popescu et al.	<u> </u>		
W,	AF11	5,65	1,125	07/1997	Witt et al.	 		
\sim	AG11	5,65	9,782	08/1997	Senter et al.	1		
(A)	AH11	5,68	9,720	11/1997	Nguyen et al.			
				FOREIG	N PATENT DOCUMENTS			
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	AI11							Yes No
	AJ11							Yes No
	AK11		-					Yes No
	AL11							Yes No
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	АМ	<u>11</u>	Smith et	a7. "Implemen	ntation of Precise Interrupt Annual International Sympo	s in Pi	pelined Pro	cessors," rchitecture,
2	AN	11	Smith, J. IEEE Tran	E. et al., "Ιπ sactions on Co	nplementing Precise Interrup Omputers, Vol. 37, No. 5, Ma	ots in P ny 1998,	ipelined Pro	ocessors," 3.
W	AO ·	11	Internati	onal Conference	on Multiple Instruction Issues on Architectural Support	e," Pro for Pro	oceedings of gramming La	the 3rd nguages and
W	AR	<u>11</u>	Pipelined	l., "Instructi Processors," , pp. 27-34.	on Issue Logic for High-Per 14 th Annual Int'l Symposium	formanc on Comp	e, Interrup outer Archit	table ecture, June
	AS	11	Thornton, Corporati	J.E., <u>Design</u> 61, 1970, pp.	of a Computer: The Control 58-140.	Data 66	00, Control	Data
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10	AA12	5,7	08,841	01/1998	Popescu et al.				
W	AB12	5,7	68,575	06/1998	McFarland et al.				
\sim	AC12	5,7	78,210	07/1998	Henstrom et al.				
W	AD12	5,7	97,025	08/1998	Popescu et al.				
\sim	AE12	5,8	32,205	11/1998	Kelly et al.				
\sim	AF12	5,8	32,293	11/1998	Popescu et al.				
ω .	AG12	4,8	39,797	06/1989	Katori et al.				
ω	AH12	5,2	33,694	08/1993	Hotta et al.			<u> </u>	
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	AI12								Yes No
	AJ12								Yes No
	AK12								Yes No
	AL12								Yes Nō
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	АМ	12	Tjaden et IEEE Tran	al., "Detecti s. On Computer	on and Parallel Executions, Vol. C-19, No. 10, Oct	of I	ndepen 1970, j	dent Inst pp. 889-8	ructions," . 95.
W	AN	12		epresentation tion), 1972, p	and Detection of Concurre	ency U	sing O	rdering M	atrices,
	AO	12			entation of Concurrency wi ol. C-22, No. 8, August 19				," IEEE :
	AR	<u>12</u>			icient Algorithm for Expl			iple Arit	hmetic
	AS	12	Purpose C	, "An Efficien ode," Proceedi iences, 1986,	nt Hardware Algorithm to E ings of the 19th Annual Ha pp. 41-50.	Extrac Iwaii	t Conc Intern	urrency F ational C	rom General- onference on
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	AA13	5,34	45,569	09/1994	Tran			<u> </u>	
w	AB13	5,98	87,593	11/1999	Senter et al.				
W	AC13	6,2	30,254	05/2001	Senter et al.				
\mathcal{C}	AD13	6,43	34,693	08/2002	Senter et al.			ļ.,	
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	AO	13	Weiss, R.	, "Third-Gener	ation RISC Processors," El	DN, Ma	irch 3	0, 1992, _]	pp. 96-108.
14	AR	13			el 486 pipelines produce p Vol. 3, No. 6, June 1989,			sor perfo	
(1/4)	AS	13	IBM Journ	al of Research	and Development, Vol. 34			hh	0, pp. 1-70.
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	ATTY. DOCKET NO. SP036.C10	APPLICATION NO. To Be Assigned
FORM PTO-1449	APPLICANT: SENTER BRASHEARS	et al.
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	FILING DATE: Herewith	GROUP: To Be Assigned

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W	AT	14	Johnson, M		r Microprocessor Design, Pr	entice Hal	.1, 1991,	pages 50-53
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\mathcal{O}	AS	<u>16</u>	English-la 33546, (Fe	nguage transla bruary 17, 190	at:	ion of Japanese Laid-Open - date of publication), 2	Pate 25 pa	ent Ap ages.	plication	No. S61-
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		APPLICATION NO. To Be Assigned
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	FILING DATE: Herewith	GROUP: To Be Assigned

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FORM PTO-1449

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. SP036.C10

APPLICATION NO. To Be Assigned

APPLICANT: SENTER BRASHEARS et al.

FILING DATE: Herewith

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	ATTY. DOCKET NO. APPLICATION NO. SP036.C10 To Be Assigned			
FORM PTO-1449	APPLICANT: SENTER BRASHEARS et al.			
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	FILING DATE: Herewith	GROUP: To Be Assigned		

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